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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/769,417	01/26/2001	Yoshio Fudeyasu	49657-962	4245

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McDERMOTT, WILL & EMERY  
600 13th Street, N.W.  
Washington, DC 20005-3096

EXAMINER
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NAMAZI, MEHDI

ART UNIT	PAPER NUMBER
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2188

DATE MAILED: 02/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/769,417

Applicant(s)

FUDEYASU, YOSHIO

Examiner

Mehdi Namazi

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 22 November 2004.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 3-17 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 4-14 and 17 is/are allowed.
- 6) ☒ Claim(s) 3 and 15 is/are rejected.
- 7) ☒ Claim(s) 16 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### DETAILED ACTION

1. This office action is in response to amendment filed November 22, 2004.

#### *Response to Arguments*

2. Applicant's arguments filed November 22, 2005 have been fully considered but they are not persuasive. With respect to applicant's arguments on pages 2-4 of the remarks (examiner considers applicant's remark with regard to claim 1 are in effect directed to claim 3).

With regard to first, and third paragraph on page 3 ("The Examiner has mischaracterized claim 1"(claim 3) or ("Motomura fails to disclose or suggest at least one output terminal, different in number from the input terminal(s) for receiving the write data, for outputting the read data", as claim 1 (claim 3)). The Examiner disagree with this statement because: claim 3 teaches "a plurality of input terminals for receiving write data, a control signal and an address signal; and at least one output terminal, different in number from output terminal(s)....." Wherein Motomura teaches "a plurality of input terminals for receiving write data, a control signal and an address signal (fig. 2C, elements 24, and 25, address signal is not shown. It is inherent in Random Access Memory to have address signal because the memory is accessed randomly, not sequentially); and at least one output terminal (fig. 2C, element 26), different in number from the input terminals for receiving the write data, for outputting read data (2 input terminals to 1 output terminal). Therefore Motomura meets the limitations of the claim 3. Motomura further teaches that INPUT terminal 25 (col. 3, line

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3) is for writing data into memory and **output** terminal 26 (col. 3, lines 3-4) is for reading data from memory.

In response to applicant's argument (second paragraph on page 3) that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., the number of bit width for each of the input/output terminals) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

Claims are given the broadest reasonable interpretation consistent with the specification and limitations in the specification are not read into the claims (*In re Yamamoto*, 740 F.2d 1569, 222 USPQ 934 (Fed. Cir. 1984)).

### ***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 3, and 15 is rejected under 35 U.S.C. 102(b) as being anticipated by Motomura (U.S. Patent No. 6,338,108).

As per claim 3, Motomura teaches a semiconductor memory device comprising: A plurality of input terminals for receiving write data, a control signal and an address signal (fig. 2C, elements 24, and 25, address signal is not shown. It is inherent in Random Access Memory to have address signal because the memory is accessed **randomly**, not sequentially); and at least one output terminal (fig. 2C, element 26),

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different in number from the input terminals for receiving the write data, for outputting read data (2 input terminals to 1 output terminal), wherein the input terminals are coupled to a first bus (fig. 2C is the request bus, where input terminals are connected to), and said at least one output terminal is coupled to a second bus (fig. 2C, is response bus, wherein output terminal is connected to), and each of the first and second buses is a unidirectional bus for transferring a signal or data in one direction (col. 3, lines 35-38).

As per claim 15, Montomura teaches the input terminals receive the write data, the control signal and the address signal at common terminals (fig. 9C, element 5).

***Allowable Subject Matter***

5. Claims 4-14, and 17 are allowed.
6. Claim 16 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
7. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

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the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mehdi Namazi whose telephone number is 703-306-2758. The examiner can normally be reached on Monday-Friday 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on 703-306-2903. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mehdi Namazi

February 2, 2005

*Mano Padmanabhan*  
2/2/05

MANO PADMANABHAN  
SUPERVISORY PATENT EXAMINER